



EP.SR.100

Entrypass Smart Reader

Features

- Slimline Reader Interface
- OSDP (Open Supervised Device Protocol)
- MIFARE Sector Read & Write
- Multi-protocols (runtime configurable)
- AES 128 Encryption
- Full size antenna
- IP65
- Reader Status Monitoring
- Support 14443A /14443B

Technical Specification

Functions \ Model	EP.SR.100
Main Processor	NXP ARM Cortex M3 @ 72MHz
RAM	8K
ROM	64K
RFID Processor	NXP PN series NFC controller
	ISO/IEC 14443A/MIFARE Reader/Writer
	Mifare DESFIRE EV1
	ISO/IEC 14443B Reader/Writer
	ISO/IEC 14443A/MIFARE Card MIFARE Classic 1K or
	MIFARE Classic 4K card emulation mode
	ISO/IEC 18092, ECMA 340 Peer-to-Peer
Card Technology	MIFARE Classic (CSN Read, Sector Read/Write) MIFARE EV1 (CSN Read, File Read/Write)

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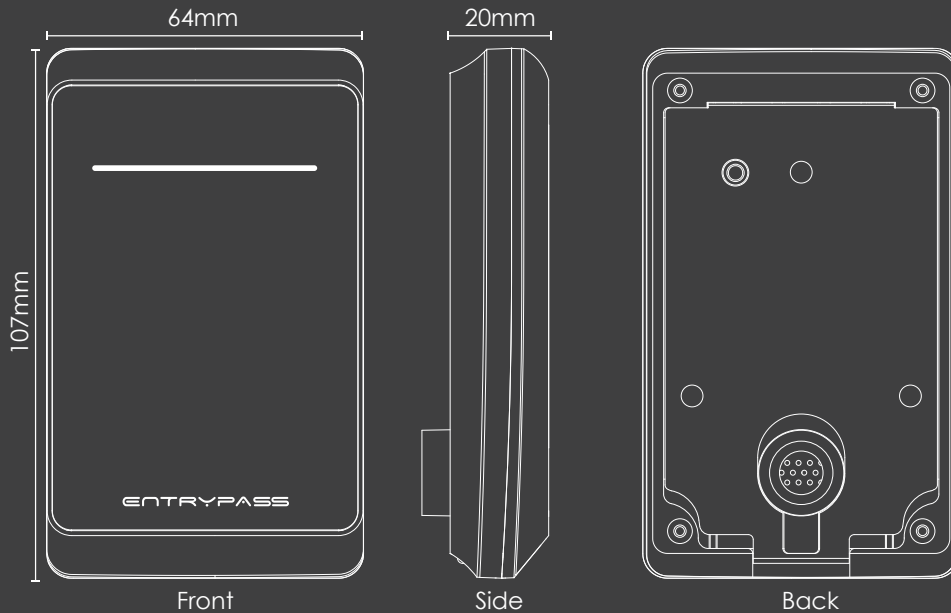
Technical Specification

Communication Interface	Wiegand (OSDP configurable)
Wiegand Output	Yes
LED Display Control	Yes (Tricolor Red,Green,Blue)
Audio Buzzer Control	Yes
Tamper Monitoring	Yes
Input Voltage	12VDC ($\pm 15\%$)
Power (@12 V input)	1.2W
Casing Material	ABS
Front Cover Plate	Coated Scratch Resistance Acrylic

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Dimension



Ordering Information

EP.SR.100

Entrypass Smart Reader Mifare Desfire Ev1



www.entrypass.net

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